

Day : Wednesday

Date: 1/12/2005
Time: 17:50:41**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = KUMATA

First Name = ICHIRO

Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
10916539	Not Issued	020	08/12/2004	LOGIC PROCESSING APPARATUS, SEMICONDUCTOR DEVICE AND LOGIC CIRCUIT	KUMATA, ICHIRO
09824969	Not Issued	071	04/03/2001	TRANSMITTING CIRCUIT AND METHOD THEREOF, RECEIVING CIRCUIT AND METHOD THEREOF, AND DATA COMMUNICATION APPARATUS	KUMATA, ICHIRO
09824040	Not Issued	030	04/03/2001	CROSSTALK CANCELLATION CIRCUIT, INTERCONNECTION MODULE, INTERCONNECTION METHOD OF AUTOMATIC INTERCONNECTION APPARATUS, AND INTEGRATED CIRCUIT	KUMATA, ICHIRO
09781109	6715010	150	02/09/2001	BUS EMULATION APPARATUS	KUMATA, ICHIRO
09129423	Not Issued	164	08/05/1998	FLIP FLOP CIRCUIT AND DESIGN SYSTEM USING THE SAME	KUMATA , ICHIRO
09039028	6127872	150	03/13/1998	DELAY CIRCUIT AND OSCILLATOR CIRCUIT USING THE SAME	KUMATA , ICHIRO
08834146	5883529	150	04/14/1997	FUNCTION CLOCK GENERATION CIRCUIT AND D-TYPE FLIP-FLOP EQUIPPED WITH ENABLE FUNCTION AND MEMORY CIRCUIT USING SAME	KUMATA , ICHIRO
08352840	5619157	150	12/02/1994	SYNCHRONIZING CIRCUIT WITH DYNAMIC AND STATIC LATCH CIRCUITRY	KUMATA , ICHIRO

Inventor Search Completed: No Records to Display.

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Search Another: Inventor	<input type="text" value="kumata"/>	<input type="text" value="ichiro"/>	<input type="button" value="Search"/>

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